

## EREC G5 Stage 2 Sub-group

### Meeting No. 4

Held at ABB Ltd, Daresbury Park, Warrington WA4 4BT

On Wednesday 12th October 2016 10:00-15:00

#### Meeting Notes

Attendee	Affiliation	Initials	Role
Forooz Ghassemi	National Grid	FGh	Guest
Ben Gomersall	National Grid	BG	Guest
Frank Griffiths	ABB	FG	Member
Andrew Oliver	TNEI	AO	Member
Simon Scarbro	WPD	SPS	Chair
Ahmed Shafiu	Siemens	AS	Secretary

Item	Topic & Note	Action
2.	<b>Agree Notes of Previous Meeting</b> Agreed.	
3.	<b>Actions from Meeting 3</b>	
3.1	ECRC Report 1681  SPS has obtained the report from EATL. SPS explained that this report underpins ETR112 (which in turn underpins the k values for 6.6kV/11kV/20kV/22kV PCC in EREC G5/4-1; namely in Table 8). The report was briefly discussed. It was noted that the test used current injection at LV and the frequency range covered 75Hz to 975Hz. Members asked that SPS see if EATL are happy for the WG Members to have a copy.	SPS
3.2	Definition of Converter Types  AS provided a definition of Active Front-end (AFE) converters with diagram. This was discussed by FG and FGh. It was agreed that a definition with diagram for 6-p and AFE converters would be sensible and that the switching devices in the diagram should be generic rather than specifically IGBTs. AS will circulate a draft.	AS
4	<b><math>\Sigma S_{equ}</math> Derivation (replacement for EREC G5/4-1 Table 6 &amp; 10)</b>	
4.1	Impact of Size & Numbers of equipment  FG explained how the assumptions affect the current emissions. The more disturbing equipment the lower the current emissions so the assumed values are conservative.	
4.2	Basis of Values in Stage 1 Table 5 & Stage 2 Table 7 Draft  SPS recapped on how the values in the existing EREC G5/4-1 Table 6 & 10 have been derived. The aggregate permitted emissions in ACE 73 Table V are based on the current required to give an incremental voltage distortion as set out in	

	<p>ACE 73 Table I and with current emissions assumed in ACE 73 Appendix C. See ACE 73 Table IV. The Table V values were used to derive the limits in EREC G5/4-1 by dividing the values by 8 for LV PCC and 6 for HV PCC. The 8 and 6 were derived by assuming 20 and 10 pieces of equally sized disturbing equipment and allowing for phase angle diversity. It was noted that the emissions based on modern analysis give higher 5<sup>th</sup> harmonic content for 6-pulse converters.</p> <p>SPS explained that the proposed values in draft Stage 1 Table 5 are based on assuming 15 pieces of existing disturbing equipment giving voltage distortion at 75% of the planning level leaving 5 items to give the remaining 25% taken together with a coincidence factor of 0.9. A similar approach was used for draft Stage 2 Table 7 based on a total of 10 pieces of disturbing equipment.</p>	
4.3	<p>IEC 61000-2-6 Typical 6-pulse values</p> <p>SPS explained that this standard has a method of calculating harmonic current emissions. SPS to show at next meeting.</p>	SPS
4.4	<p>Example LV background measurement data – impact on assumptions</p> <p>SPS showed background measurements from a location in central Bristol. In this particular case the 5<sup>th</sup> harmonic background level was 2.84% and so not far from the 75% of the 4% Planning Level. However, the 25<sup>th</sup> harmonic was 0.0047% and very much less than 75% of the 0.7% Planning Level. For this example case the assumption that voltage distortion levels for the 25<sup>th</sup> harmonic are conservative.</p>	
5	<b>Stage 1 &amp; 2 Draft</b>	
5.1	<p>Stage 1 &amp; 2 Draft – Update</p> <p>SPS explained the major changes to the draft text, highlighting the changes to Fig 2 and aggregation in particular.</p>	
5.2	<p>Aggregation &amp; Derivation of <math>\sum I_{equ}</math> values in Fig 2</p> <p>SPS explained how the <math>\sum I_{equ}</math> values had been derived. Example:</p> <p><b><u>1-phase Equipment, Service Current Capacity <math>\geq 100A</math> Example</u></b></p> <ul style="list-style-type: none"> <li>Assumed source impedance @ supply terminals = 0.25 + j 0.25 ohm (see Table 2).</li> <li>1-phase fault current phase-neutral @ supply terminals = 230V/(0.25 + j0.25 ohm) = 650.538A.</li> <li>Assumed source impedance @ PCC/source impedance @ supply terminals ratio = 0.8.</li> <li>1-phase fault current phase-neutral @ PCC = 650.538A/0.8 = 813.173A.</li> <li>For 16A max equipment <math>I_5 = 1.14A</math> from IEC 61000-3-2 Class A table.</li> <li><math>V_{hc}\%</math> @ PCC for 16A rated equipment = <math>I_h \times h \times k \times 100\% / I_{f\text{ph-n}}</math> @ PCC = <math>1.14A \times 5 \times 1 \times 100\% / 813.173A = 0.70\%</math>.</li> <li>Planning level for 5<sup>th</sup> = 4%.</li> <li>25% Planning Level for 5<sup>th</sup> = 1%.</li> <li>Assumed Coincidence Factor for current = 0.9.</li> <li>Effective limit = 1%/0.9 = 1.111%.</li> <li>Aggregate rated current giving <math>V_{hc}\% = 1\%</math> is <math>16A \times 1.111\% / 0.70\% = 25.39A</math> rounded to 25A.</li> </ul>	
5.3	<p>Aggregation &amp; Derivation of <math>\sum I_{equ}</math> values in Fig 3</p> <p>SPS explained how the <math>\sum I_{equ}</math> values had been derived. Example given was:</p> <p><b><u>3-phase Equipment, Service Current Capacity <math>&lt; 100A</math> Example</u></b></p>	

	<ul style="list-style-type: none"> <li>Assumed source impedance @ supply terminals = <math>0.24 + j 0.15</math> ohm (see Table 3) = 0.2830 ohm.</li> <li>Assumed source impedance @ PCC/source impedance @ supply terminals ratio = 0.8.</li> <li>3-phase fault current @ PCC = <math>400V (\sqrt{3} \times 0.8 \times 0.283 \text{ ohm}) = 1019.98A</math>.</li> <li>Rating implied by this fault current for equipment compliant with IEC 61000-3-12 with <math>R_{sce} = 33</math> is <math>1019.98A/33 = 30.9A</math> rounded to 31A.</li> </ul>	
5.4	<p>Stage 1 &amp; 2 Draft – Review Comments</p> <p>The comments from AO and FGh/BG were reviewed.</p> <p>See attachments with inserted comments and responses.</p> <p>Actions arising from AO comments:</p> <ul style="list-style-type: none"> <li>Add reference to Note 2 in Figure 2. <span style="float: right;">SPS</span></li> <li>Add reference to Note 1 in Figure 3. <span style="float: right;">SPS</span></li> <li>Amend title of column 1 in Table 4a and 4b to ‘Aggregate Equipment Rating <math>\sum I_{equ}</math> (A)’. <span style="float: right;">SPS</span></li> <li>Amend title of column 1 in Table 4a and 4b to ‘Aggregate Equipment Rated Apparent Power <math>\sum S_{equ}</math> (kVA)’. <span style="float: right;">SPS</span></li> <li>Reference Stage 1D, AS agreed to obtain data for Siemens AFE of similar rating and pass to FG for analysis. <span style="float: right;">AS/FG</span></li> <li>Amend Stage 2A title to ‘...Aggregate Equipment Rated Apparent Power <math>\sum S_{equ}</math>’. <span style="float: right;">SPS</span></li> <li>Amend Stage 2B title to ‘...Aggregate Equipment Rated Apparent Power <math>\sum S_{equ}</math>’. <span style="float: right;">SPS</span></li> <li>It was agreed that an example should be added to the Worked Examples document that covers the application of the Thevenin equivalent in Stage 2C. FG agreed to prepare an example. <span style="float: right;">FG</span></li> <li>Units in formulae need to be defined and be consistent. SPS to amend text. <span style="float: right;">SPS</span></li> <li>It was agreed that under Stage 2C it is necessary to check all harmonic orders. SPS to amend text. <span style="float: right;">SPS</span></li> <li>Post-meeting note: Note that the CDV for IEC 61000-2-2 (77A/926/CDV) proposes having CL for <math>h &gt; 40</math> (i.e. 2kHz) up to 150kHz. In the range up to 9kHz (i.e. <math>h = 180</math>) limits are given in 200Hz bands. This will add a complication to assessment.</li> </ul> <p>Actions arising from FGh/BG comments:</p> <ul style="list-style-type: none"> <li>SPS to check whether the 32A, 22A, 42A &amp; 25A in Table 2 and Fig 2 would change if other harmonics were checked. <span style="float: right;">SPS</span></li> <li>Reference Fig 3, SPS to consider whether, in addition to the 3-phase and 1-phase cases, it is necessary to cover interphase equipment (400V phase-phase equipment) or split phase (460V case). <span style="float: right;">SPS</span></li> <li>Reference Fig 3, ‘Table 3a/3b’ should be ‘Table 4a/4b’. SPS to amend. <span style="float: right;">SPS</span></li> <li>SPS to review whether the approach of ensuring <math>R_{sce} \geq 33</math> in Stage 1B needs modification given that the harmonic current emissions permitted by IEC 61000-3-12 for <math>R_{sce} = 33</math> result in voltage distortion exceedances for some harmonics based on the Table 8 ‘k’ values. <span style="float: right;">SPS</span></li> <li>SPS to amend symbol for current source. <span style="float: right;">SPS</span></li> </ul>	

5.5	<p>Stage 1 &amp; 2 Draft Worked Examples - Review</p> <p>Time did not permit a review of the worked examples. This was deferred to the next meeting.</p>	
5.6	<p>Contrast with draft G5 v8-1 text</p> <p>This was deferred to the next meeting.</p>	
6	<p><b>Agree Further Work</b></p> <p>Save for the actions recorded above, this item was deferred until the next meeting.</p>	
7	<p><b>AOB</b></p> <p>This was deferred to the next meeting.</p>	
7.1	<p>IEC TR 61400-21-3 Thevenin Model</p> <p>This was deferred until the next meeting.</p>	
7.2	<p>77A/926/CDV: Amendment 1 to IEC 61000-2-2 Ed.2</p> <p>This was deferred until the next meeting.</p>	
7.3	<p>Maintenance of IEC 61000-3-12</p> <p>This was deferred until the next meeting.</p>	
8	<p><b>Future meetings</b></p> <ul style="list-style-type: none"> <li>• Dates</li> </ul> <p>The date of the next meeting was agreed as 9 November. Venue to be arranged by BG to be at Warwick.</p> <p>Post-meeting Note: Now arranged at National Grid, Warwick.</p> <ul style="list-style-type: none"> <li>• Agenda items</li> </ul> <p>Not discussed.</p>	BG: